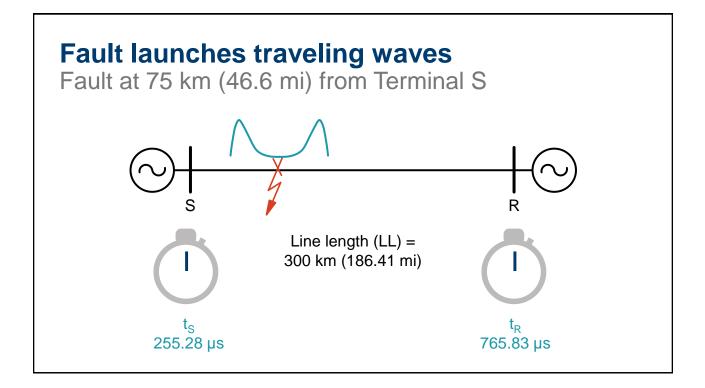
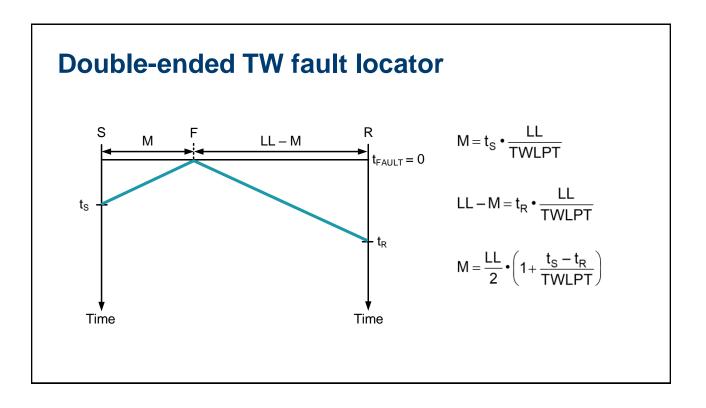
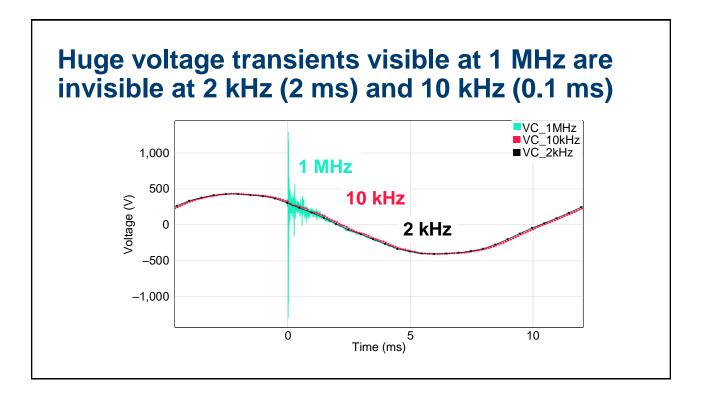


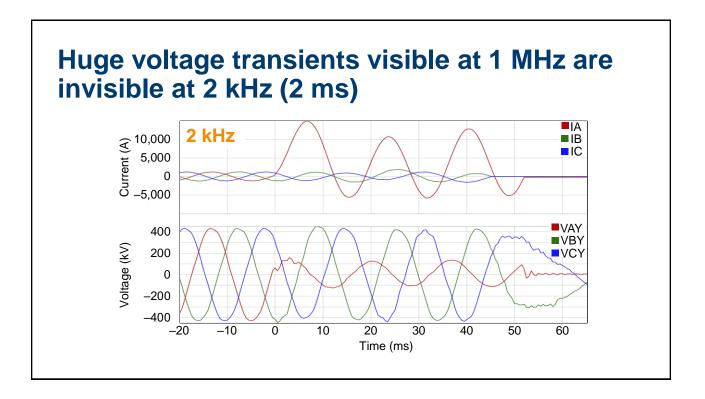
## Submicrosecond timekeeping

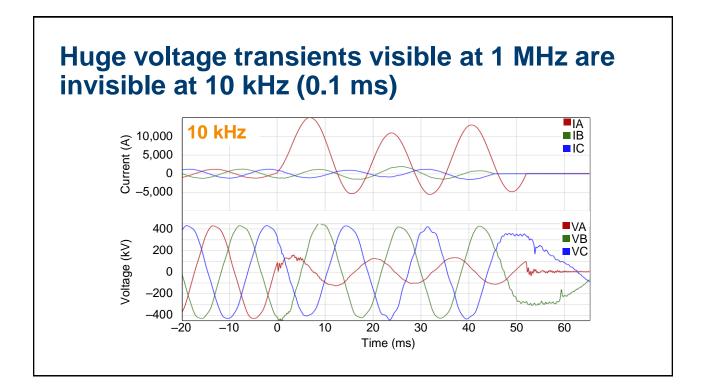
- Antenna connected satellite-synchronized clock time source
- IRIG-B clock with IEEE C37.118 time code extensions
- IRIG-B signal 100 Hz edge jitter under 500 ns
- Time source time error of less than 10 µs

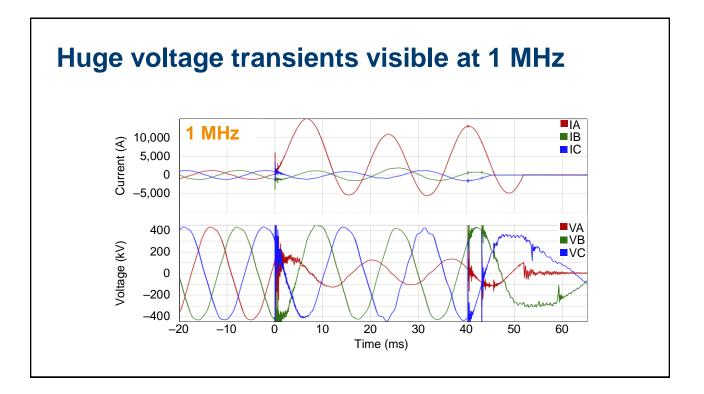


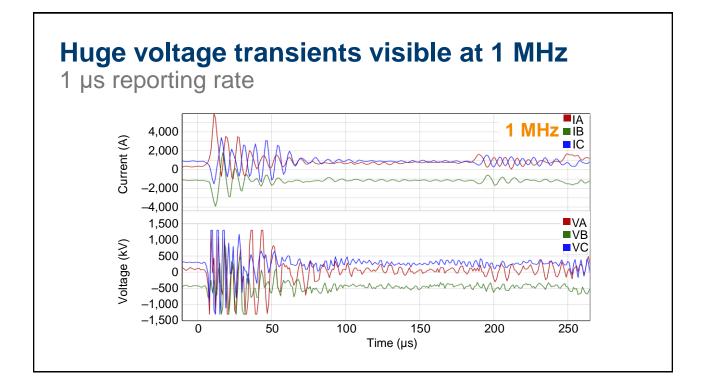


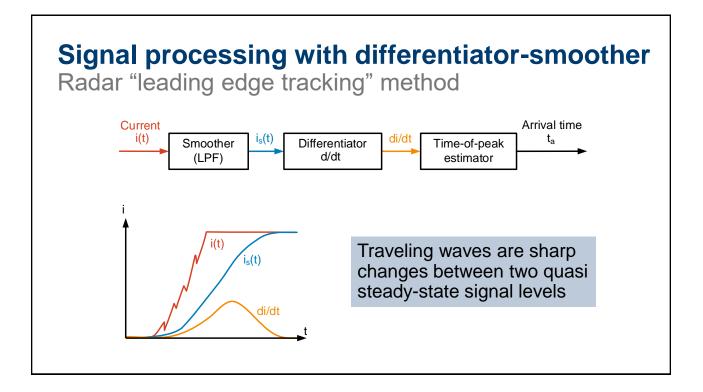


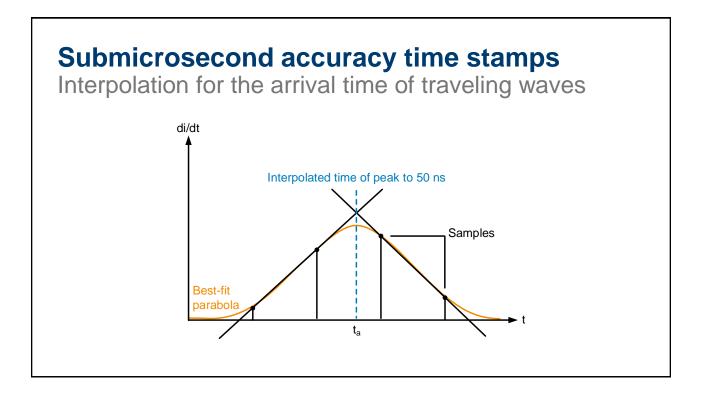


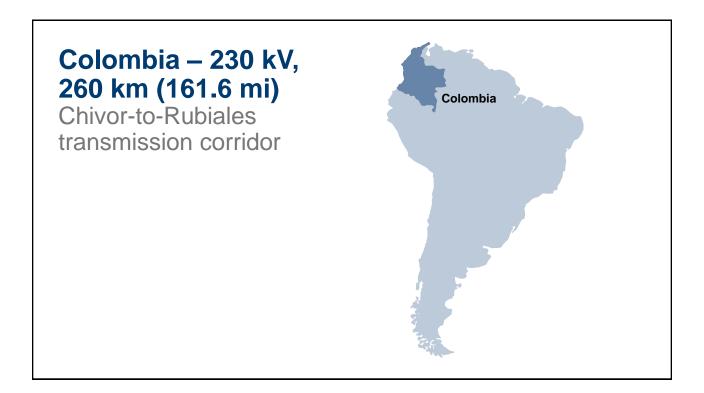




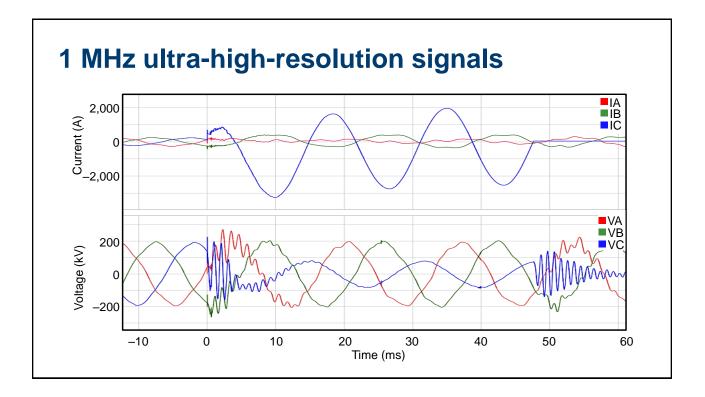


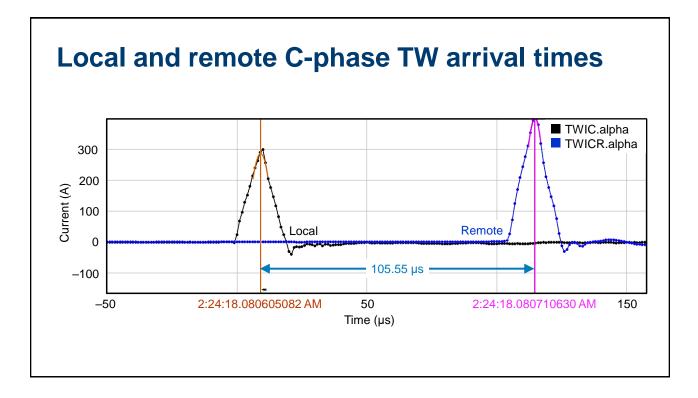


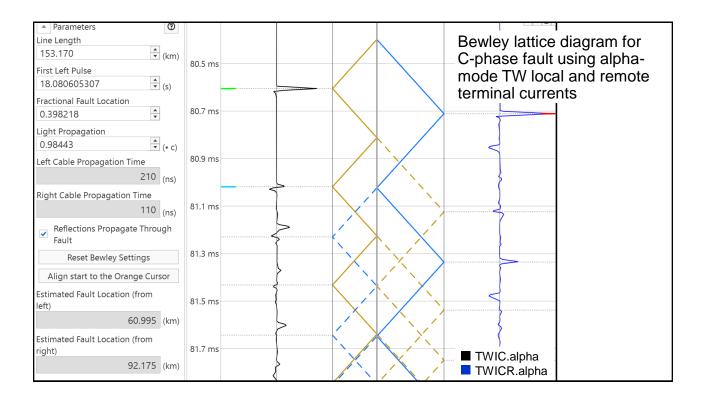






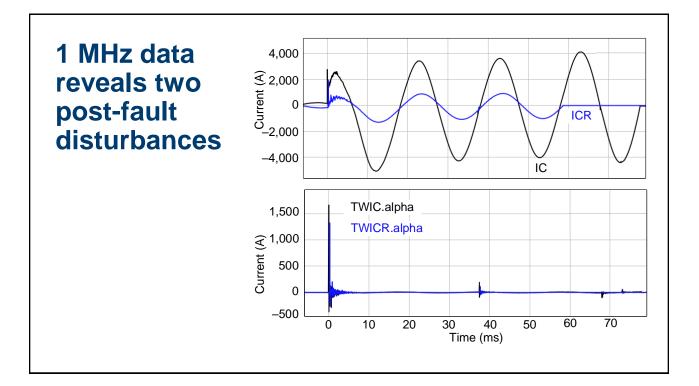


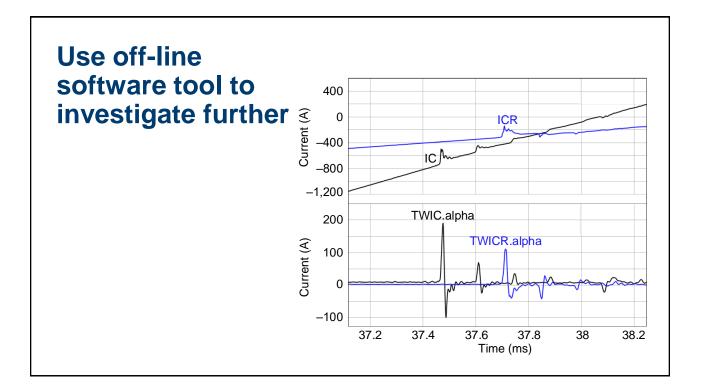


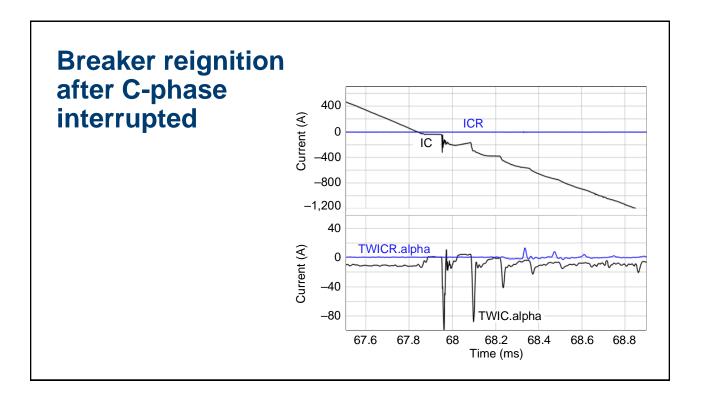


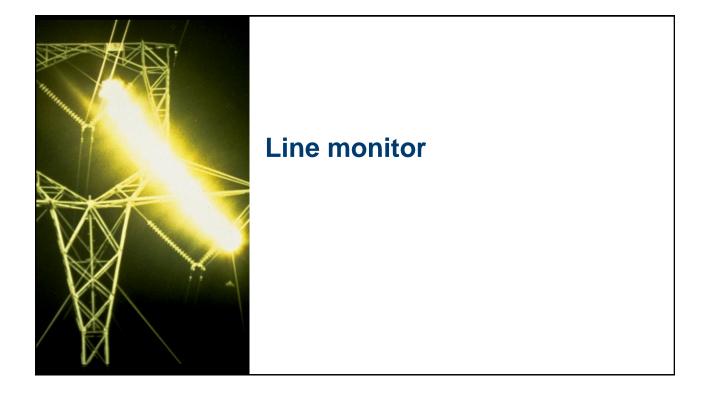


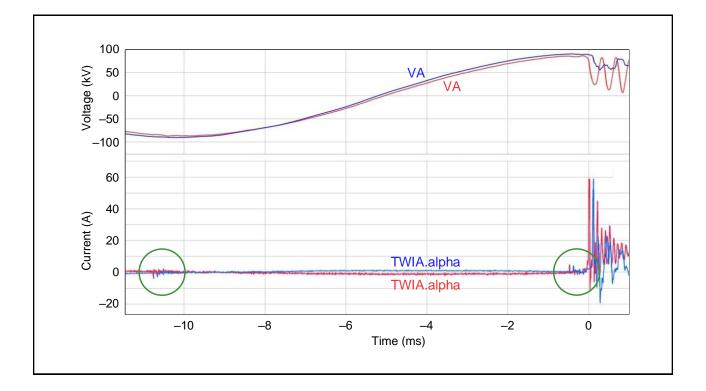
## **Circuit breaker reignition**

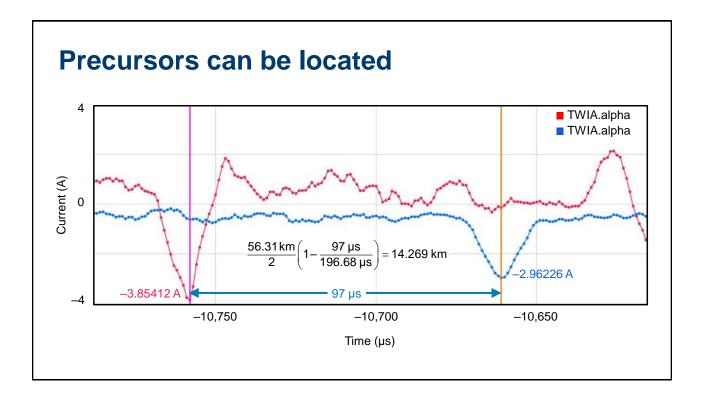


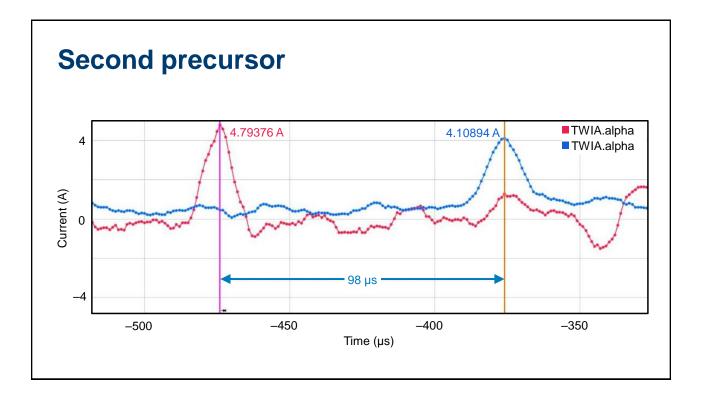


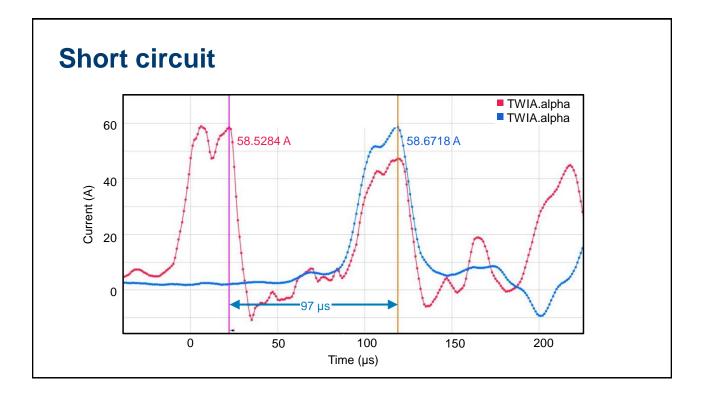












## Line monitor counts low-energy and fault events and asserts alarm

- Trigger double-ended TW fault locator, even if no trip occurs
- Calculate event location if no trip occurs, and calculate fault location if trip occurs
- Tabulate event and/or fault locations over time in 0.25-mile or kilometer bins
- Assert alarm or trip on high bin counts

