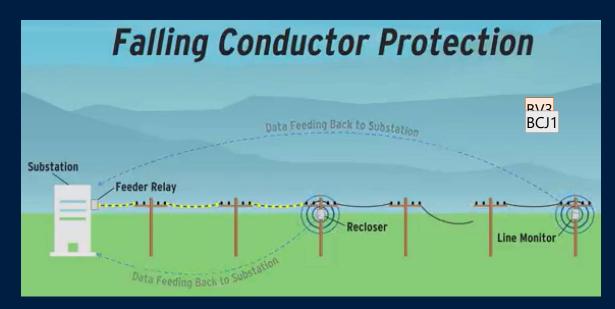
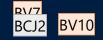
Catching Falling Conductors in Midair – Detecting and Tripping Broken Distribution Circuit Conductors at Protection Speeds





Dan Dietmeyer – Sr. Engeveleer San Diego Gas & Electric®





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Slide 1

BV1	Add register symbol (R) circle R Brody, Valerie, 3/7/2019
BCJ4	Done. Bolton, Christopher J, 3/7/2019
BV2	copyright line TBD Brody, Valerie, 3/7/2019
BCJ3	This was vetted back in 2016 and likely allowed because we are a part of a joint patent with SEL and Quanta on this technology Bolton, Christopher J, 3/7/2019
BV3	Does SDG&E own this artwork? Brody, Valerie, 3/7/2019
BCJ1	Yes, this is ours Bolton, Christopher J, 3/7/2019
BV7	Add the SDG&E logo. Let me know if you need a logo file. Brody, Valerie, 3/7/2019
BCJ2	Yes, could you please send it? Bolton, Christopher J, 3/7/2019
BV8	Replace copyright line with: Brody, Valerie, 3/12/2019
BV9	(c) 2019 San Diego Gas & Electric Company. Trademarks are property of their respective owners. All rights reserved. Brody, Valerie, 3/12/2019
BV10	Add SDG&E logo Brody, Valerie, 3/12/2019

SDG&E[®]Overhead Distribution System

- Approximately 6,500+ miles of overhead distribution line infrastructure
- Grounded three- and four-wire systems
- Nominally 12kV and 4kV
- High penetration of distribution PV requires new solutions for monitoring, protection, and control

Slide 2

BV4	Register symbol Brody, Valerie, 3/7/2019	
BCJ5	Done.	

Bolton, Christopher J, 3/7/2019

Advanced SCADA Project Applications More Than 60 Use Cases Defined

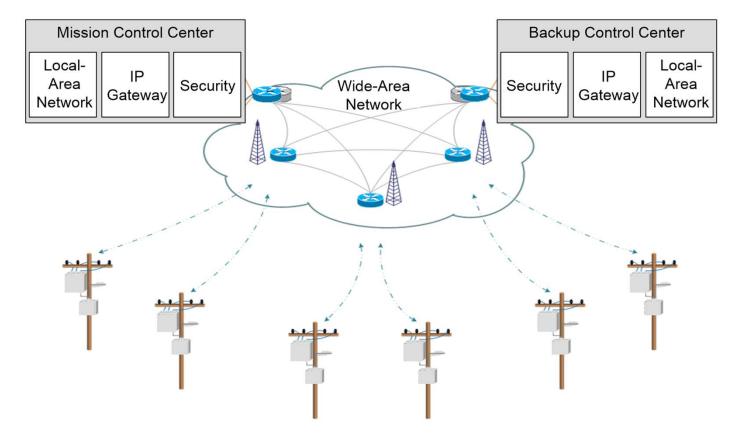
- Falling conductor protection (patented)
- Driven by high penetration of distribution PV
- Voltage profile monitoring and control
- Selective load shedding and restoration
- Power quality monitoring
- Apparatus and system condition monitoring

Advanced SCADA Features

- Increased accuracy of voltage and current
- Phase angle measurements across circuit
- GPS time-stamped data
- 30 synchrophasor samples per second for fast measurement (60 samples/sec in the future)
- IEC 61850 GOOSE messaging for real-time control
- Remote engineering access and event reports
- Advanced security features

SCADA System Architecture Traditional

BV/5 BCJ6



Slide 5

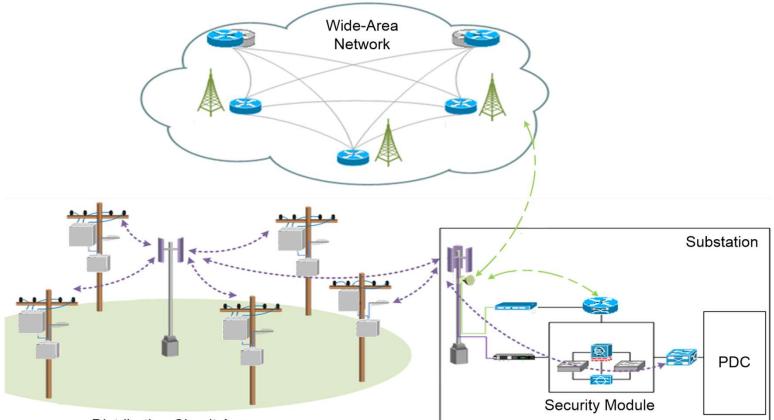
BV5 Does SDG&E own this artwork?

Brody, Valerie, 3/7/2019

BCJ6 Yes.

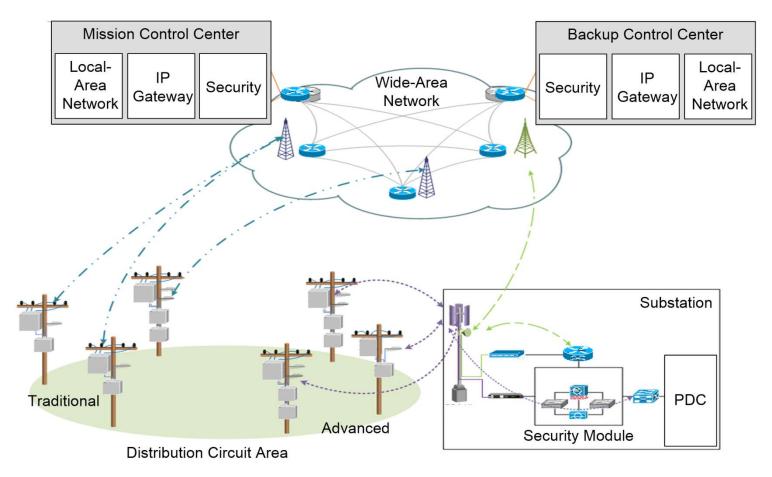
Bolton, Christopher J, 3/7/2019

SCADA System Architecture Advanced

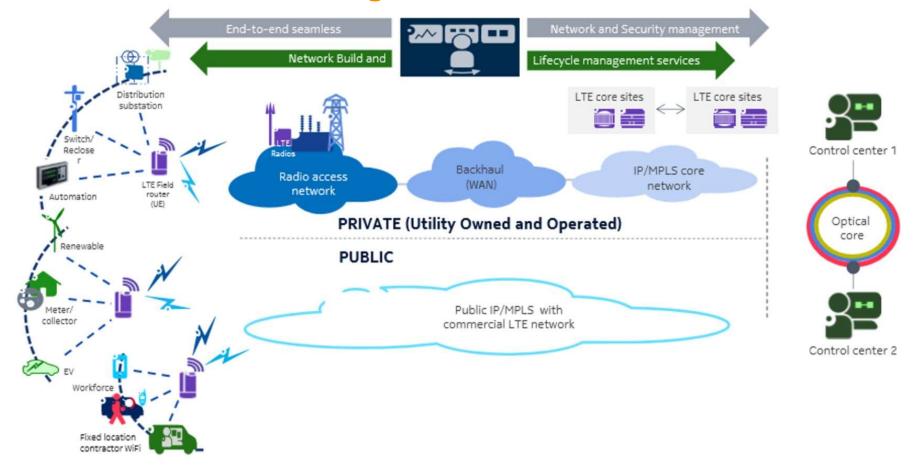


Distribution Circuit Area

SCADA System Architecture Traditional and Advanced Overlay

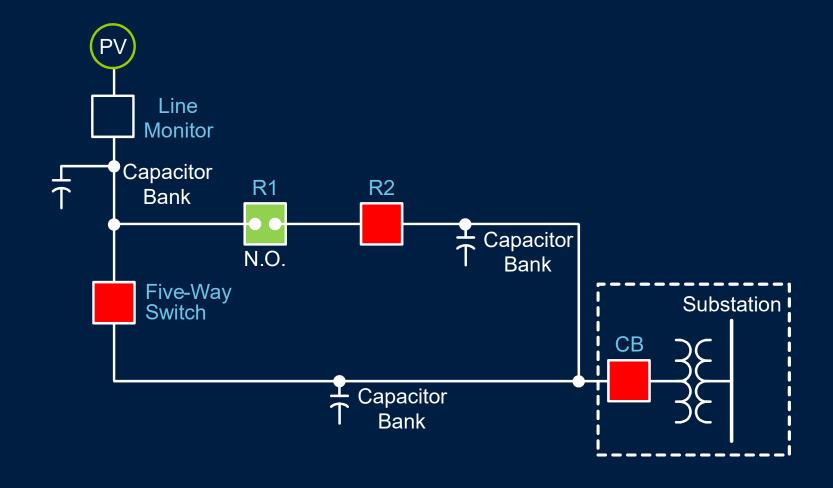


Private Long Term Evolution (LTE) Advancing Communication Making FCP More Secure



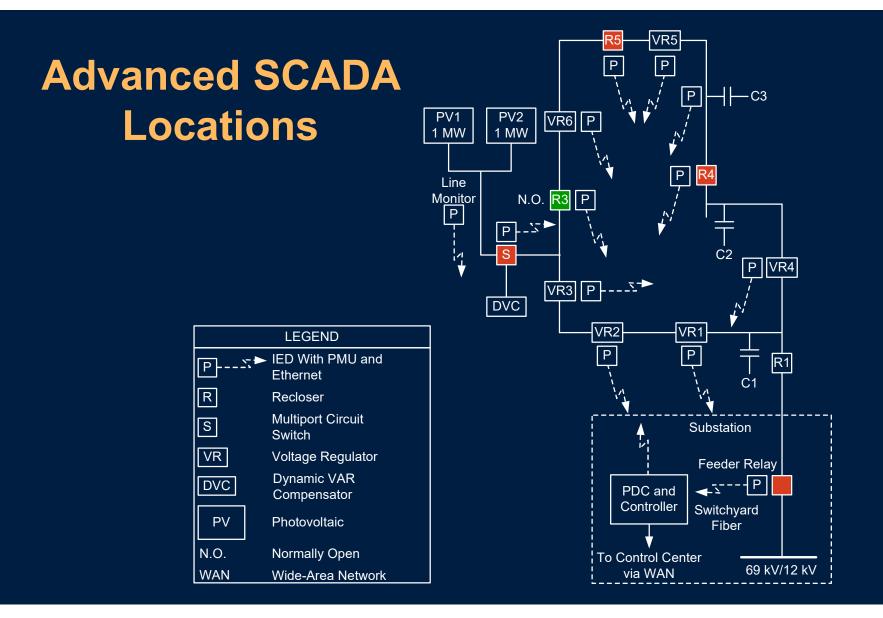
SDG&E Typical Feeder

вv/11 BV12

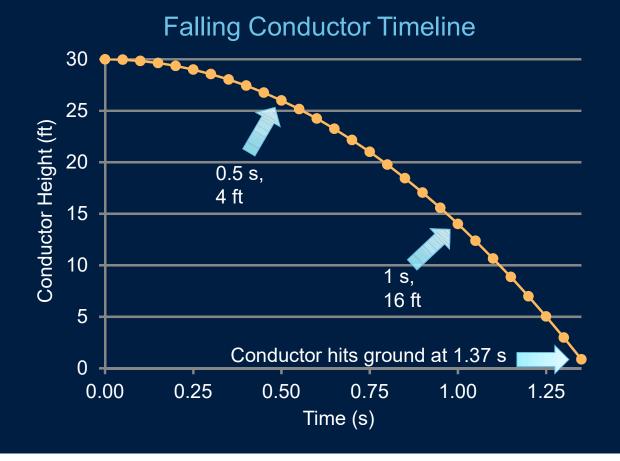


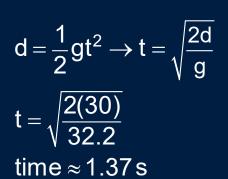
Slide 9	
BV11	For slide 9 and on, wherever there are graphs that were developed jointly, please put this line in the lower left corner: Brody, Valerie, 3/12/2019
BV12	The illustrations herein were jointly developed by San Diego Gas & Electric, Quanta Technology, and Schweitzer Engineering Laboratories.

Brody, Valerie, 3/12/2019



Detect Broken Conductor and Trip Circuit Before Line Hits the Ground?





BV6 BCJ7

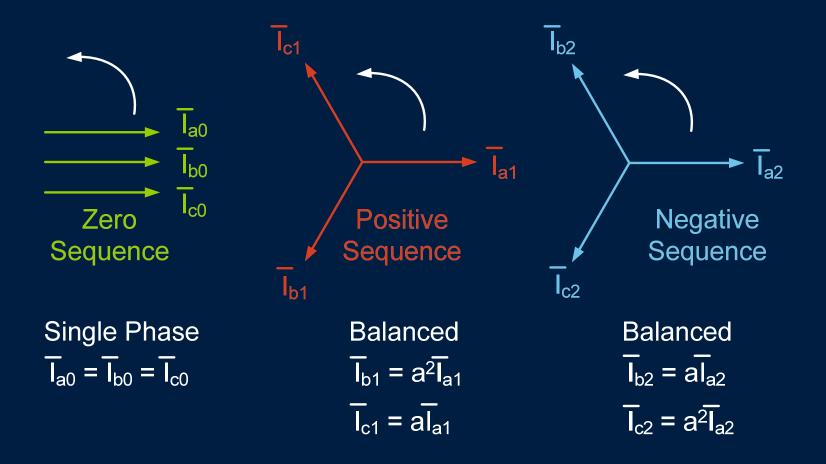
Slide 11

BV6 Do you have proper usage/license rights to use this image? If not, please delete it. Brody, Valerie, 3/7/2019

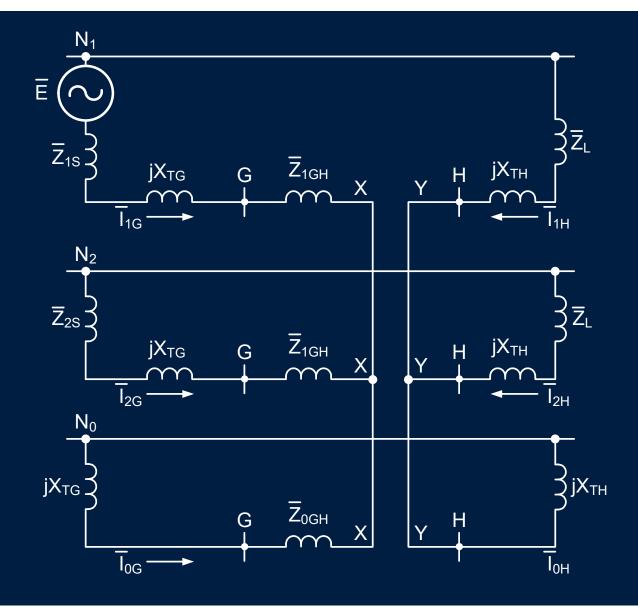
BCJ7 Done.

Bolton, Christopher J, 3/7/2019

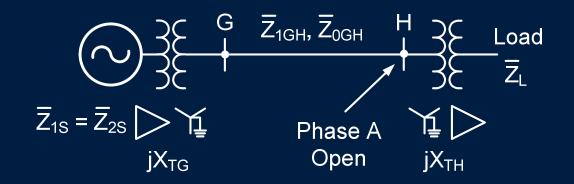
Sequence Components Analysis



Open-Phase Analysis



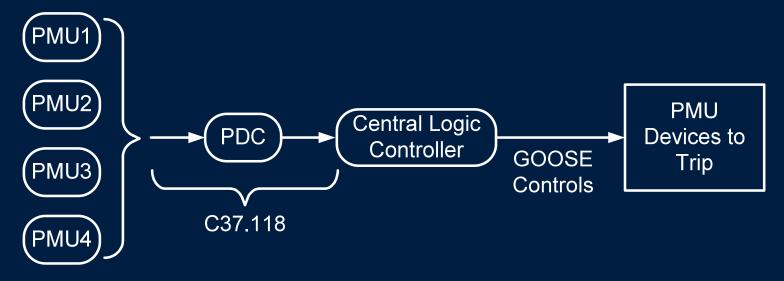
Open-Phase Analysis



 $V_{NOM} = 1 \text{ p.u.}$ Before Break $V_{0} = 1 \text{ p.u.}$ Phase A Break $V_{0} = 1 \text{ p.u.}$ $V_{1} = 1 \text{ p.u.}$ $V_{2} = 0.5 \text{ p.u.}$ $V_{1} = 1 \text{ p.u.}$

Detection Methods

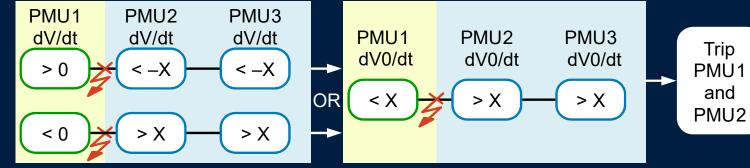
- dV/dt (change detection)
- V0 and V2 magnitude
- V0 and V2 angle



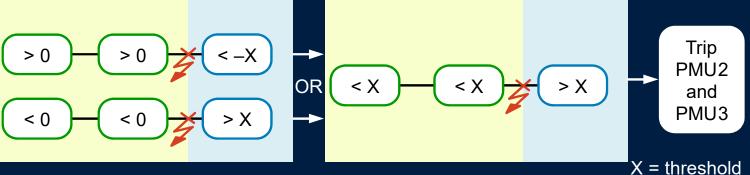
dV/dt Method

Conductor Break dV0/dt Supervision Check

Between PMU 1 and PMU 2

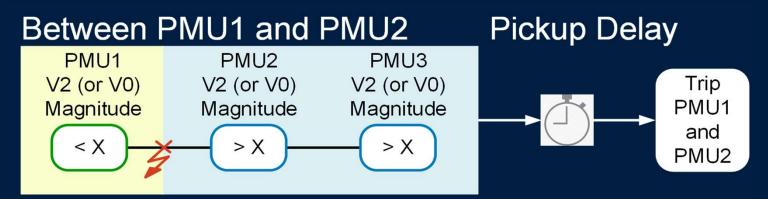


Between PMU 2 and PMU 3

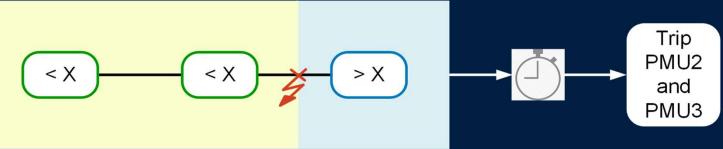


V2 and V0 Magnitude Method

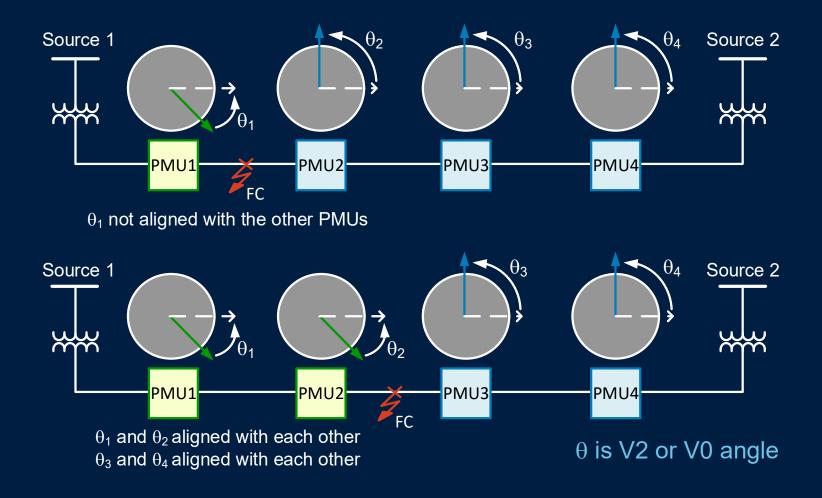
Conductor Break

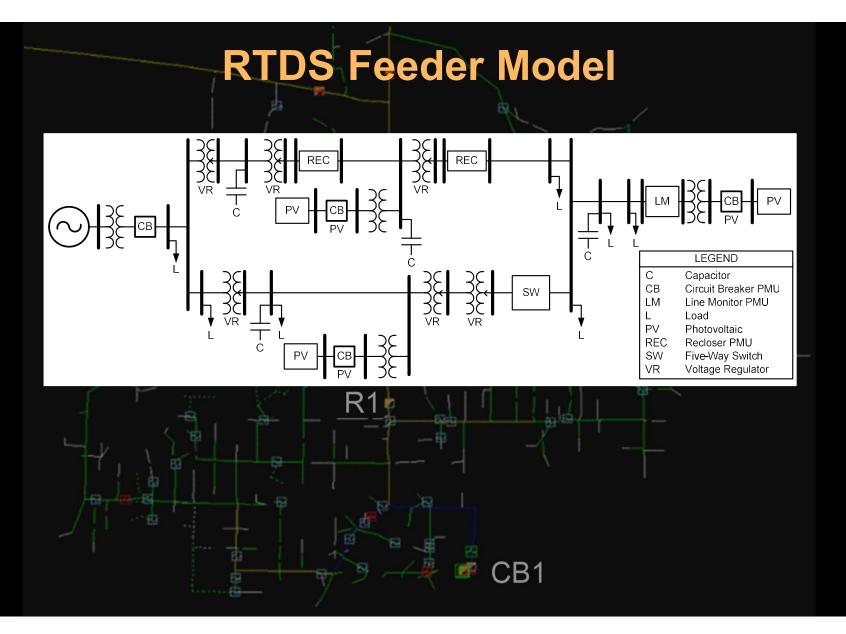


Between PMU2 and PMU3



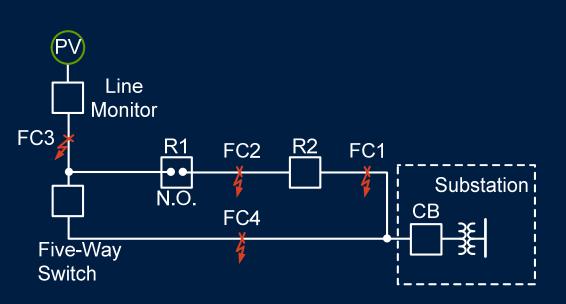
V2 and V0 Angle Method





Example Lab Test Results

PV	PV Off, Loop Open					
Load %	FC1	FC2	FC3	FC4		
100	3	3	3	3		
75	3	3	3	3		
25	3	3	3	3		

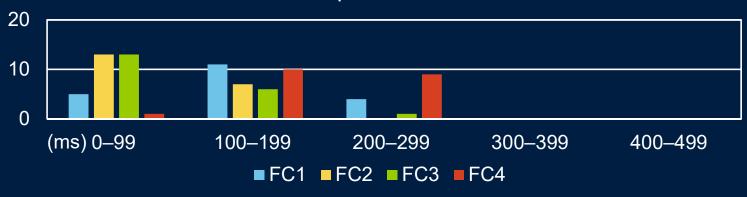


PV On, Loop Open					
Load %	PV%	FC1	FC2	FC3	FC4
100	100	3	3	3	3
	75	3	3	4	4
	50	3	3	3	3
	25	3	3	3	3
	100	3	3	3	3
25	75	3	3	3	3
	50	3	3	3	3
	25	3	3	3	3

Arc Speed and Results Comparison Number of Test Cases Versus dV/dt Pickup Times



Arc Speed = 5 m/s



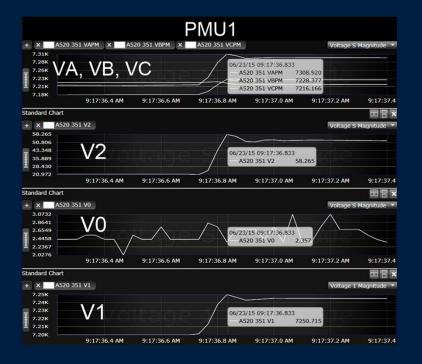
Security Testing

- Capacitor bank switching
- Voltage regulator tap unbalance
 - Angle method for $\approx 4.5\%$ voltage (6 taps)
 - V0 magnitude method for ≈ 10% voltage (15 taps)
- Largest single-phase load switching
- PV operation
- Internal / external faults

Results Detection Screen

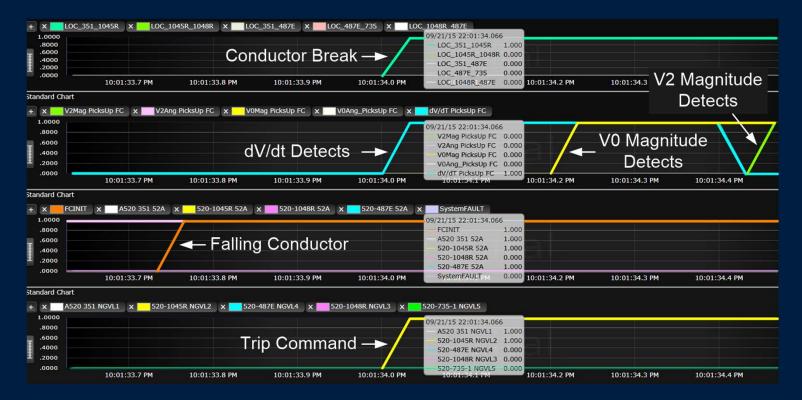


Inverter Response



- Conductor break between PMU1 and PMU2
- PV inverter source ON

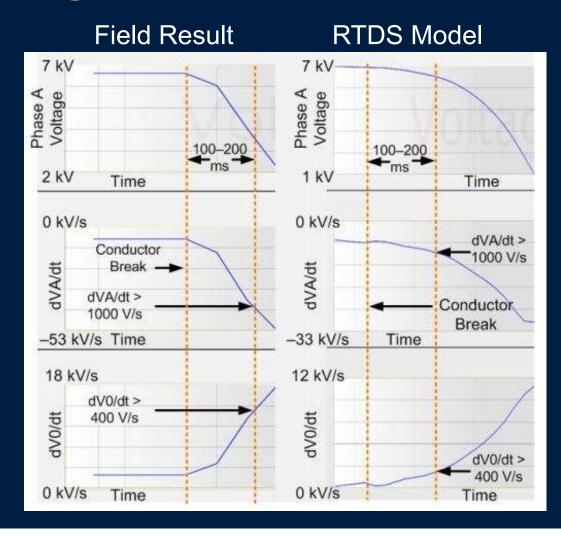
Results dV/dt and Magnitude Methods



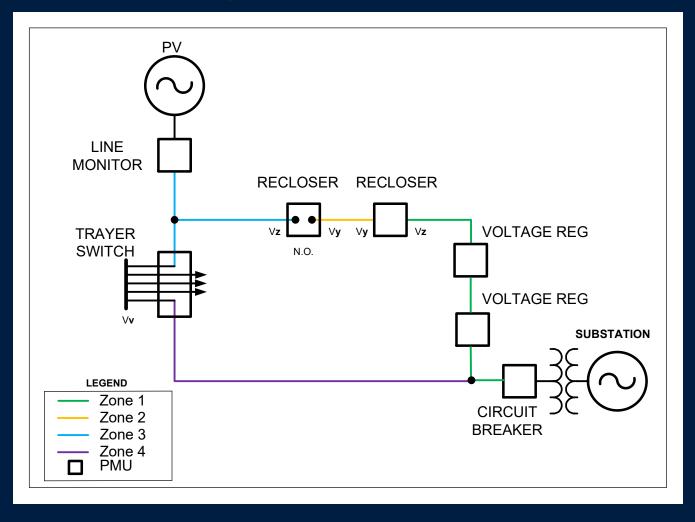
Field Installation and Testing

- First system installation in January 2015
- Falling Conductor Protection (FCP) in monitoring mode
- Simulation of conductor breaks with disconnect switch opening on recloser
- 100% correct operation
- Ethernet radio tuning required

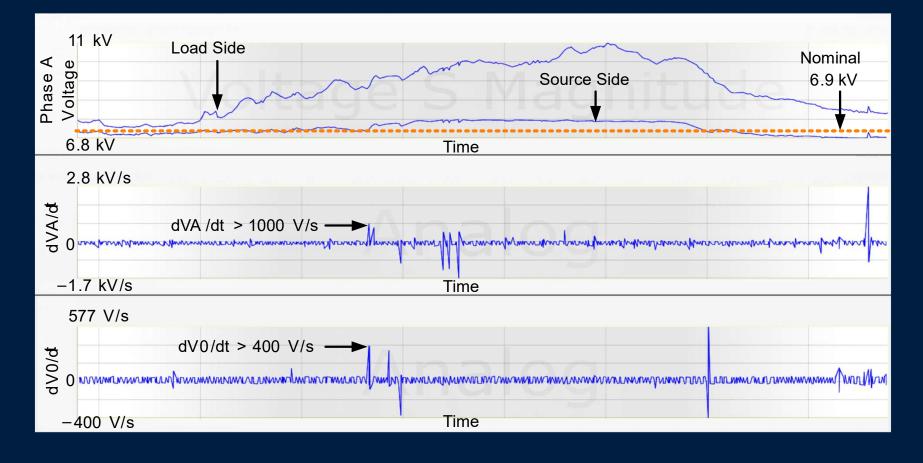
Breaking Arc – Field Versus Lab Tests



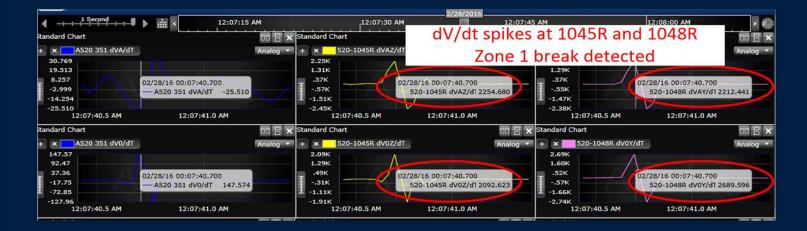
Falling Conductor Zones



Synchrophasors show detailed circuit behavior Capacitive voltage sensor discoveries



Zone 1 dV/dt Operation



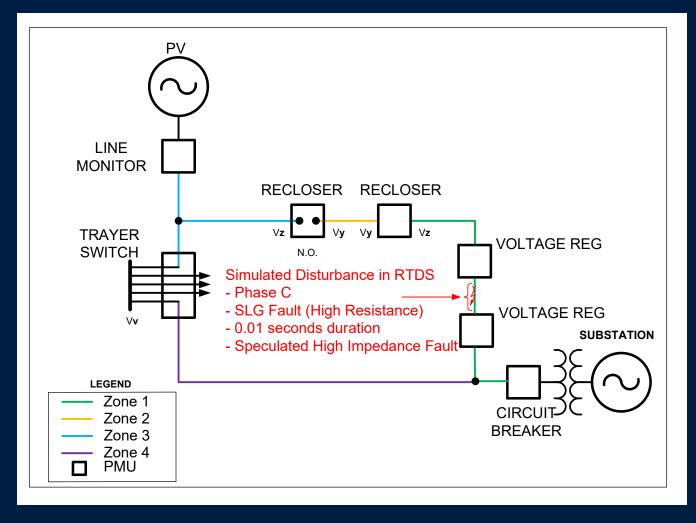
- Field Event 28th Feb 2016
- FC detected by dV/dt between CB and R1

Zone 1 Current Spikes

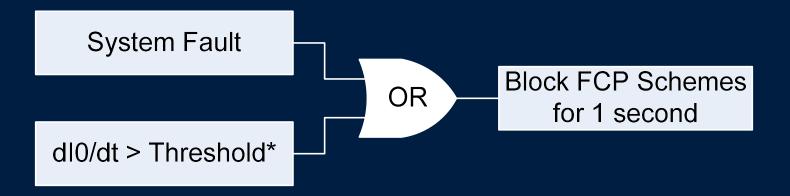
2/28/2016 12:07:4	5 AM	12:0	8:00 AM	> 🔮
Standard Chart			·····	
+ × A520 351 IAPM × A5	20 351 IBPM 🗙 📃 A5	520 351 ICPM	Current	1 Magnitude 🔻
193.53 182.01 170.48	Phas	se-C current	spikes at CB	PMU
158.96 147.44 135.91 12:07:40.6 AM	02/28/16 00:07:40.7 — A520 351 IAPM — A520 351 IBPM	140.882 154.750 41.0 AM	12:07:41.2 AM	12:07:41.4
Standard Chart 🧧	-A520 351 ICPM	193.530		
x 520-1334 JA x 520-1334 IB x 520-1334 JC				Current 1 Magnitude
520-1334 IA = 38.5 A	Phas	e-C current	spikes at VR	1
IB= 38.5 A			02/28/16 00:07:40.700 	
IC = 92.3 A				
4.589				
x 520-1225 IA x 520-1225 IB x 520-1225 IC 6.154				Current I Hagnitude
s.144 520-1225	No curr	rent spikes o	bserved at V	VR2
IA = 25.3 A IB = 26.1 A	enu l	Magn	02/28/16 00:07:40.700 	
IC = 16.1 A				
16.102				

- Current spikes observed at CB and VR1, but not at VR2
- Indicates temporary fault between VR1 and VR2

RTDS Simulation

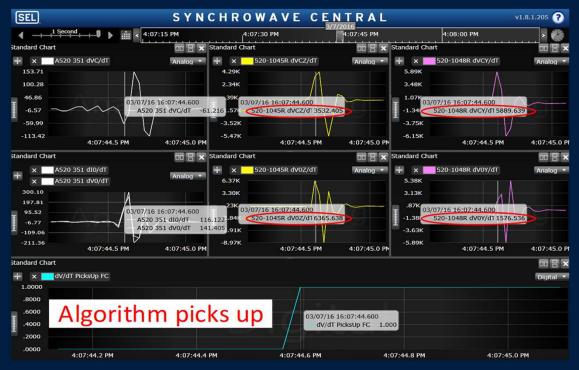


dl0/dt Supervision



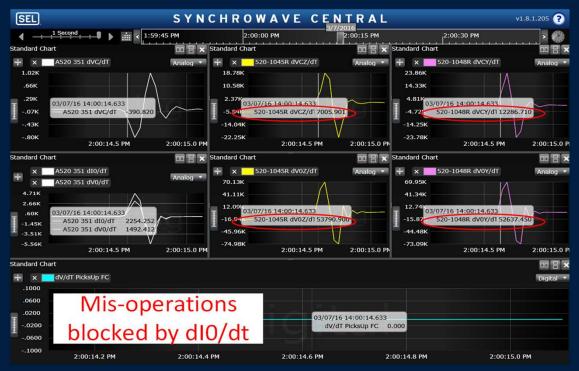
- Threshold based on RTDS testing results
- dI0/dt spikes at CB PMU used to block falling conductor detection algorithms
- Temporary faults can be blocked using this supervision

Lab Simulation – Before dl0/dt



- Zone-1 mis-operation confirmed in lab
- dl0/dt block not implemented
- Mis-operation similar to field event

Lab Simulation – After dl0/dt



- Zone-1 mis-operation simulated in lab
- dI0/dt blocks Falling Conductor scheme
- System abnormal alarm condition

System Protection is a Balancing Act

- **SPEED** FAST TO MINIMIZE DAMAGE
- SENSITIVITY RELAY SEES FAULT
- SELECTIVITY REMOVE FAULTED ELEMENT ONLY
 - SECURITY DO NOT TRIP FALSELY
 - SIMPLICITY SIMPLE CONTROL SCHEMES

FCP Compliments Existing Layers of Protection

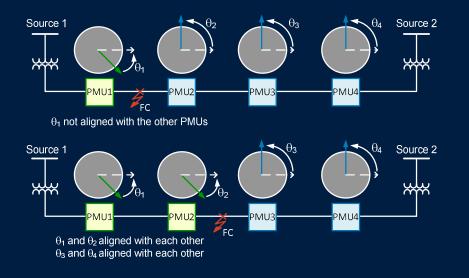
- FCP Falling Conductor Protection detects break in conductor
 - Fastest trips before the fault
 - Coordination FCP should be first
- Overcurrent Time and Instantaneous
 - Simple coordination
- SGF Sensitive Ground Fault detects high-impedance ground fault
 - Slow 3.5 to 5.5 seconds
- Advanced SGF More sensitive than SGF using adaptive set point, spike counting, and/or harmonics
 - Slower > 5 seconds

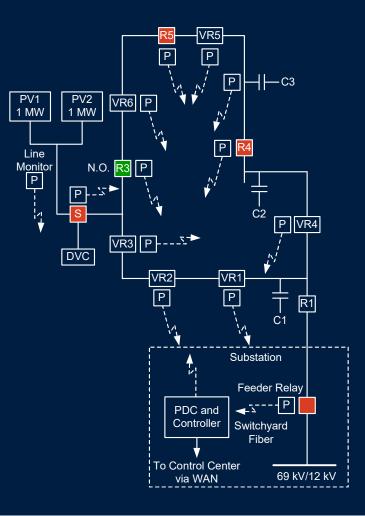
FCP Limitations

- Does not detect wire down without break
- Needs fast, secure, and available communication path to circuit PMUs
- Uses voltage from each protected circuit path end a journey of years for coverage
- Learning features of new technology

Ease of Application

- Key requirement achieved no circuit-dependent application settings
- FCP logic only needs topology of circuit and PMU IEDs





Summary

- Advanced SCADA has 60 use cases including FCP
- FCP isolates broken conductors in 0.2 0.5 s (half the distance to the ground) preventing the fault
- FCP is dependable in lab test including high PV penetration
- FCP mitigates HILP events <u>fire and hazard reduction</u>
- Confidence built from secure and reliable field performance
- Compliments existing protection
- Scalable design needs only circuit layout information

Next Steps

- FCP of first equipped circuit commissioned on 11/18/2016
- Additional circuits equipped and commissioned in 2017-2019
- Pursuing ongoing work to reduce fire risk and enhance public safety
- Installing new IEDs with PMU capable devices with moderate additional cost
- SDG&E will be well positioned for future PV penetration
- Overall goal of enabling FCP on 100% of HFTD Tier 2 and Tier 3 circuits

Questions?