

IEEE C37.118 -2011.2 P9 Update

- Weekly meetings (mostly) every Thursday 4:30 PM Eastern Time
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- FRACSEC
 - Now a 24-bit integer. Possible switch to integer value sample index of sample rate
- Event driven data frames
 - For breaker and other events that don't occur on synchronous frequency divisions
- STAT Flag Bits
 - Expand to 32-bits (16 each for PMU and PDC)
 - Clearly specify which device can set which bits
 - Present
 - Bit 15–14 Data error:
 - 00 = good measurement data, no errors
 - 01 = PMU error. No information about data
 - 10 = PMU in test mode (do not use values) or absent data tags have been inserted (do not use values)
 - 11 = PMU error (do not use values)
 - Bit 13 PMU sync 0 when in sync with a UTC traceable time source
 - Proposed
 - Bit 15 set only by PDC 0 = Good, 1 = Data Late
 - Bit 14 set only by PMU 0 = Good, 1 = Test Mode
 - **Bit 13 set only by PMU 0 = in sync with a UTC traceable time source**

