

### Performance Evaluation and Review of System Protection Scheme Design with the help of Synchrophasor Measurements

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#### Outline



• WAMS in India – Progress Update

System Protection Schemes in India

Case Studies- Performance Evaluation and Review of SPS

#### Summary

### WAMS in India 2015





22-Mar-16

### WAMS in India 2016





22-Mar-16

# System Protection Schemes (SPS) in India



•Total Number of SPS in India : 129\*

#### •Wide Impact SPS in WR Grid

<b>Control Actions</b>	Total	Percentage
Load Rejection	20	59%
Generation Rejection	7	21%
Generation and Load Rejection	6	18%
HVDC Control	1	3%

#### Local Impact SPS in WR Grid

<b>Control Actions</b>	Total	Percentage
Load Rejection	34	92%
Generation Rejection	3	8%





#### Performance Evaluation and Review of SPS Case Study-I

### Assessing Speed of the SPS



#### Assessing Speed of the SPS contd.





t=0 Occurrence of an Event







t<sub>2</sub>=11 min 13 sec 724 msec

SPS-3 criteria satisfied Envisaged Action : Units unloading

### Assessing Speed of the SPS contd.



#### Delay observed in Unit Unloading

•Observation through PMU

### **Revised SPS Design & Performance**

Performance of Modified SPS

Successful Operations : 7

- SPS Logic Modified
- Delay in unit unloading is rectified







### Performance Evaluation and Review of SPS Case Study-II



# Assessing Dependability of the SPS contd.





#### •Number of Operations : 24\*\*\*\*\*\*

- Successful Operations : 23
- Unsuccessful Operations : 0
- Unnecessary Operations : 1
- Failures : 0

#### SPS is disabled

• SPS triggered irrespective of power flow direction



#### Performance Evaluation and Review of SPS Case Study-III



# Assessing Dependability of the SPS contd.







# SPS Design Case Study-IV

### SPS Design

#### •Tripping of any one of four Ckts, oscillations are observed in Grid

#### Tripping of other Ckt on Power Swing





### SPS Design Contd.

#### •Four Analog, Four Digital Inputs



Blocking tripping of other line on Power Swing





#### Summary

- •Synchrophasor measurements aided in assessing performance of SPS
- •Synchrophasor measurements helped System Operator in reviewing SPS
- •Synchrophasor measurements assisted in taking corrective actions to prevent undesirable or delayed SPS actions
- Using Synchrophasor measurements characteristics of SPS
  - Dependability
  - Speed
  - Sensitivity
  - are Evaluated



#### References



[1] System Protection Schemes In Power Networks, CIGRE Task Force, June 2001

[2] Industry Experience with Special Protection Schemes , IEEE Transactions on Power Systems, Vol. 11, No. 3, August 1996

[3] Deployment of System Protection Schemes for Enhancing Reliability of Power System, International Conference on Power System 2011, IIT Madras, 22-24 Dec 2011

[4] Synchrophasors – Initiative in India, June 2012, POSOCO

[5] Synchrophasors – Initiatives in India December 2013, POSOCO

[6] Report on System Protection Schemes, May 2015, POSOCO

[7] <u>www.posoco.in</u>



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