Interoperability Test of IEEE C37.118 Standard-based PMUs

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Interoperability Test for IEEE C37.118 Standard-based PMUs

- **1.** Introduction
- 2. IEEE C37.118 Standard
- **3.** Interoperability Test Method for PMUs
- 4. PMU Interoperability Test Suite & Test Cases
- **5.** Interoperability Tests for Commercial PMUs
- 6. Summary



How not to do interoperability





1. Introduction



Example of Wide Are Monitoring, Protection and Control System (WAMPCS)

(Source: Electric Power Research Institute (EPRI))

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1. Introduction (Cont'd)



1. Introduction (Cont'd) Challenges of WAMPCS:

Challenge 1: How to make thousands of PMUs to work together? Challenge 2: How to exchange, share synchrophasor's data to achieve data interoperability?

Solution: standardizing synchrophasor data formats, communication protocols and interfaces

However: PMUs may not be interoperable even if they conform to the IEEE C37.118 standards. The main reasons are:

- the standard has mandatory and optional functions;
- some definitions in the standard are ambiguous, not clear; and
- different developers or implementers may have different interpretations of the standards, which result in different implementations.

1. Introduction (Cont'd)

Challenges of WAMPCS

Challenge 3: How to assure that these standard-based PMUs produced by different vendors can work together to guarantee the interoperability of PMUs?

Solutions:

- develop interoperability test methods for PMUs;
- conduct Plugfests or interoperability tests of PMUs; and
- certify PMUs based on interoperability tests conducted by laboratories accredited by organizations such as the IEEE-SA Conformity Assessment Program (ICAP).



2. IEEE C37.118 Standard

PMU Standard	Descriptions
IEEE 1344-1995	defined three kinds of messages: data frames, header frames and configuration messages
IEEE C37.118-2005	replaced IEEE 1344-1995, which included both measurement requirements and real-time data transfer requirements.
IEEE C37.118.1a-2014	defined synchrophasor measurement requirements including dynamic performance
IEEE C37.118.2-2011	defined synchrophasor data transfer requirements. Extended IEEE C37.118-2005 with a new CFG-3 message and a continuous time quality (CTQ) field in the SYNC word.



2. IEEE C37.118 Standard (Cont'd)

IEEE C37.118.2 Message & Communication Protocol

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3. Interoperability Test Method for PMUs

Interoperability (IEEE):

 The ability of two or more systems to exchange information and to use the information that has been exchanged.

The PMU can be interoperable with the PDC if:

- the PDC can send the Requests to the PMU based on the IEEE C37.118 standard;
- the PMU can receive, understand the Requests from the PDC, take the corresponding action based on the request, then return the Responses to the PDC based on the Requests of the IEEE C37.118 standard;

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- the PDC can receive, understand the **Responses** from the PMU; and
- the PMU and PDC can interact jointly to execute specific functions or tasks.

PDC	Request (Command)	
	IEEE C37.118 Standard	
	Response	

3. Interoperability Test Methods for PMUs (Cont'd)

- Interoperability test is a process to verify that two or more implementations of the same standard by two systems or devices can interoperate.
- Interoperability test of PMU is a process to verify if PMU (server implementation) can be interoperable with the PMU Tester (client implementation) based on IEEE C37.118.
- PMU Tester: An implementation of C37.118.2 client, which can send the C37.118 Request to the DUT (PMU) and receive the C37.118 Response from the PMU. PDC is a kind of PMU Tester.



3. Interoperability Test Method for PMUs (Cont'd)

Architecture for Interoperability Test Method for PMUs



3. Interoperability Test Methods for PMUs (Cont'd)

PMU Interoperability Test Suite & Test Cases for C37.118.2

Interoperability		Test Case	Test Case	Command	Response
Levels		No.	Name	ID	
		1	Send CFG-1 Frame	0100	CFG-1 Frame
Level 2	Level 1	2	Send CFG-2 Frame	0101	CFG-2 Frame
		3	Send Header Frame	0011	Header Frame
		4	Turn on Transmission of Data Frames	0010	Data Frame
		5	Turn off Transmission of Data Frames	0001	None
		6	Send CFG-3 Frame	1000	CFG-3 Frame

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Interoperability Test Case of Turn on Transmission of Data Frames



Interoperability Test Procedures Turn on Transmission of Data Frames



Test Case of Turn on Transmission of Data Frames



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PMU Connection Tester Screenshot

Interoperability Analysis: Test Procedures

File Edit View Go Capture Analyze Statistics Telephony Iools Internals Help Image: Statistics Telephony Iools Internals Help Internals Iools Iools Iools Iools Iools Iools Iools Internals Iools								
● ●								
Filter: Expression Clear Apply Save No. Time Source Destination Protocol Length Info 1641 91.644969 192.92.92.120 192.92.92.104 192.92.92.104 SYNCHROPHASOR 72 Command Frame, data transmission on 1642 91.649772 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1643 91.650456 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1645 91.651001 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame I G45 91.651001 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame I G45 91.651001 192.92.92.105 SYNCHROPHASOR 170 Data Frame I G45 91.651001 192.92.92.104 192.92.92.02 SYNCHROPHASOR 170 Data Frame I Frame 1641: 72 bytes on wire (576 bits), 72 bytes captured (576 bits) IIII IIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIIII								
No. Time Source Destination Protocol Length Info 1641 91.644969 192.92.92.120 192.92.92.104 SYNCHROPHASOR 72 Command Frame, data transmission on 1642 91.649772 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1643 91.650456 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1645 91.651001 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1645 91.651001 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1645 91.651001 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1645 91.651001 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1645 91.651001 192.92.92.120 SYNCHROPHASOR 170 Data Frame 170 The second								
1641 91.644969 192.92.92.120 192.92.92.104 SYNCHROPHASOR 72 Command Frame, data transmission on 1642 91.649772 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1643 91.650456 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1645 91.651001 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * * *								
1642 91.649/72 192.92.92.104 192.92.92.120 SYNCHROPHASOR 1/0 Data Frame 1643 91.650456 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame 1645 91.651001 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame Frame 1641: 72 bytes on wire (576 bits), 72 bytes captured (576 bits) B Ethernet II, Src: Dell_36:87:bc (f0:1f:af:36:87:bc), Dst: National_17:79:0e (00:80:2f:17:79:0e)								
1045 91.050430 192.92.92.104 192.92.92.120 STACHAOPHASOR 170 Data Frame 1645 91.651001 192.92.92.104 192.92.92.92 STACHAOPHASOR 170 Data Frame Image: State of the								
<pre></pre>								
B Frame 1641: 72 bytes on wire (576 bits), 72 bytes captured (576 bits) B Ethernet II, Src: Dell_36:87:bc (f0:1f:af:36:87:bc), Dst: National_17:79:0e (00:80:2f:17:79:0e)								
# Ethernet II, Src: Dell_36:87:bc (f0:1f:af:36:87:bc), Dst: National_17:79:0e (00:80:2f:17:79:0e)								
🗄 Internet Protocol Version 4, Src: 192.92.92.120 (192.92.92.120), Dst: 192.92.92.104 (192.92.92.104)								
Transmission Control Protocol, Src Port: 64881 (64881), Dst Port: 4712 (4712), Seq: 181, Ack: 110935, Len: 15								
IEEE C3/.118 Synchrophasor Protocol, Command Frame								
Synchronization word: Uxad41								
Framesize: 18								
PMU/DC ID number: 4								
SOC time stamp: Jul 15, 2015 19:23:54.00000000 UTC								
□ Time quality flags								
= Leap second occurred: False								
0 = Leap second pending: False								
0000 = Time Quality indicator code: Normal operation, clock locked (0x00)								
Fraction of second (raw): 71393								
\odot command data 0010 - command: data trapemicsion on (0y0002)								
Checksum: 0x26df [correct]								
0000 00 80 2f 17 79 0e f0 1f af 36 87 bc 08 04 50								
🖯 🐮 IEEE C37.118 Synchrophasor Protocol (synph Packets: 3155 · Displayed: 3155 (100.0%) · Load time: 0:00.060 Profile: Default								

Steps	Description	Pass/Fail (P/F)
1	Start Network Sniffer to capture packets	Ρ
2	Start PMUTester	Р
3	Send a "Turn on Transmission of Data	Ρ
	Frames" command from the PMUTester to	
	the DUT	
4	Send a "Data Frames" response from the	Ρ
	DUT to the PMUTester	
5	Wait for 30 seconds for data streaming	Ρ
6	Stop data streaming and PMUTester	Ρ
7	Stop capturing packets	Ρ
8	Analyze PMU interoperability	Ρ
9	Output results	Ρ

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Interoperability Analysis: Data Frame Command Message

	Data Frame Command Message							
pmu4-connection-test.pcap [Wireshark 1.12.6 (v1.12.6-0-gee1fce6 from master-1.12)]	No.	Field	Size	Va	lue			Pass/Fail (P/F)
<u>File Edit View Go Capture Analyze Statistics Telephony Iools Internals Help</u>	L		(byte	es)				
◉ ◉ ∡ ≡ ∡ ⊨ ≞ ೫ ₴ ! < + + + 7 ½ ! 🗐 🗐 € < 0 ፻ 🗑 ೫ ! 🛱	1	SYNC	2	AA	41			Р
Filter: Expression Clear Apply Save		Duto No		Suno		Value		Dass (Fail (D/F)
No Time Source Destination Protocol Algorith Info		Byte No. Sync						
1641 91 644969 192 92 92 120 192 92 92 104 SYNCHROPHASOR 72 Command Frame data transmission on		2 (Eramo tupo	and		0 for this standard	AA 0		P
1642 91.649772 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame		version)	anu	Bits 6-1	Frame Type	100: Command F	rame (A)	P B
1643 91.650456 192.92.92.104 192.92.92.120 SYNCHROPHASOR 170 Data Frame		versiony	-	Bits 3–0	Version number	0001.037 118-2	005(1)	P
1043 91.051001 192.92.92.104 192.92.92.120 STNCRKOPHASOK 170 Data Praile				5113 5 0	Version number	0001.037.110 2	005(1)	
■ Frame 1641: 72 bytes on wire (576 bits), 72 bytes captured (576 bits)	2	FRAMESIZE	2	00	12 (18 bytes)			Р
B Ethernet II, SrC: Dell_36:8/:DC (f0:1f:af:36:8/:DC), DSt: National_1/:/9:0e (00:80:2f:1/:/9:0e) B Internet Protocol Version 4, SrC: 192.92.92.120 (192.92.92.120), Dst: 192.92.92.104 (192.92.92.104)	3	IDCODE	2	00	014 (4)			Р
B Transmission Control Protocol, Src Port: 64881 (64881), Dst Port: 4712 (4712), Seq. 181, Ack: 110935. Len: 18	4	SOC	4	55	55 A6 B3 4a (Jul 15, 2015 19:23:54.00000000 UTC)			Р
IEEE C37.118 Synchrophasor Protocol, Command Frame	5	FRACSEC & TQ	4	01	16 e1 (71393)			Р
		Flag						
0001 = Version: IEEE C37.118-2005 initial publication (1)								
Framesize: 18 PMI/DC TD number: 4	1	Byte No. FR	RACSEC	and Time qu	uality flag		Value	Pass/Fail (P/F)
SOC time stamp: Jul 15, 2015 19:23:54.00000000 UTC		4 Bit	Bits 31–24:		7: Reserved		0	Р
E Time quality flags		(Time		me quality flag) 6		n	0	Р
.0 = Leap second direction: False			5: LS Occurred 0			0	Р	
					4: LS Pending	[0	Р
0000 = Time Quality indicator code: Normal operation, clock locked (0x00)					3–0: TQ India	ator	0000	Р
Fraction of second (raw): 71393		1-3 Bits: 23–00 (FRACSEC) 24-bit intege		r number	01 16 e1 (71393) P		
		LI			,		, v	,
Checksum: 0x26df [correct]	6	CMD	2	00	10 (2)			D
		CIVID	Z	00	10 (2)			
0000 00 80 2f 17 79 0e f0 1f af 36 87 bc 08 00 45 00/.y6E.		CMD	Co	mmand Tvn)e	PAS	S/Fail	
0020 Sc 68 fd 71 12 68 cb 5f 0f 99 7a 1c 37 5f 50 18 \h.q.hz.7_P.		0010	Da	ata frame on	(0x0002)	P	oy : un	
0030 /1 05 39 C0 00 00 at 41 00 12 00 04 55 at 05 4a					<u>,,.,</u>			
🕒 🐮 IEEE C37.118 Synchrophasor Protocol (synph 🛛 Packets: 3155 · Displayed: 3155 (100.0%) · Load time: 0:00.060 🔹 Profile: Default	7	EXTFRAME	0-65	518				N/A
NYANA TA	8	СНК	2	0x	26 df (correct)			Р

Interoperability Analysis: Data Frame Response Message



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Test Report of Turn on Transmission of Data Frames

Test case No.		4						
Test case name		Turn on Transmission of Data Frames						
Functional statement		The PMUTester sends a "Turn on Transmission of Data Frames" command to the DUT (PMU), the DUT shall continuously						
		sends "Data Frame" responses back to the PUMTester, after the DUT receives this command from the PMUTester.						
Functional Requirement		Mandatory						
Standard reference	es	IEEE C37.118.2-2011						
Test Purpose		To verify if the DUT shall continuously sends "Data Frame" responses back to the PMUTester, after the DUT receives a						
		"Turn on Transmission of Data Frames" command from The PMUTester.						
Test Resource	Hardware	One or a few PMUs						
		A network switch						
		A Laptop computer for PMUTester						
		One Laptop computer for network sniffer and synchrophasor packet analyzer						
	Software	PMUTester software (e.g. PMU Connection Tester)						
		A network sniffer (e.g. Wireshark)						
		A synchrophasor packet analyzer (application software)						
Test setup		See Figures in previous slides						
Test conditions		 Network protocol: TCP (port 4712), UDP (port 4713), TCP/UDP over Ethernet, or RS 232 						
		Nominal frequency: 50 Hz or 60Hz						
		Report rate: specified in Table 1						
		Test parameters: voltages, currents						
Test operators		Eugene Song						
Test date		9/10/2015						

Preliminary Results of Interoperability Test of IEEE C37.118-2005 Standard-based Commercial PMUs

Interoperability Test		PMU1 (TCP)	PMU2 (TCP)	PMU3 (TCP)	PMU4 (TCP)	PMU5 (UDP)	PMU6 (TCP)	PMU7 (TCP)	PMU 8 (TCP)	
Turn Off	Procedures	Р	Р	Р	Р	Р	Р	Р	Р	
	Command	Р	Р	Р	Р	Р	Р	Р	Р	
	Response	Р	Р	Р	Р	Р	Р	Р	Р	
Turn On	Procedures	Р	Р	Р	Р	Р	Р	Р	Р	
	Command	Р	Р	Р	Р	Р	Р	Р	Р	
	Response	Р	Р	Р	Р	Р	Р	Р	Р	
Header	Procedures	Р	Р	Р	Р	Р	<u>F</u>	Р	Р	• •
	Command	Р	Р	Р	Р	Р	Р	Р	Р	т
	Response	Р	Р	Р	Р	Р	<u>F</u>	Р	Р	
CFG-1	Procedures	Р	Р	Р	Р	Р	Р	Р	Р	
	Command	Р	Р	Р	Р	Р	Р	Р	Р	
	Response	Р	Р	Р	Р	Р	Р	Р	Р	
CFG-2	Procedures	Р	Р	Р	Р	Р	Р	Р	Р	
	Command	Р	Р	Р	Р	Р	Р	Р	Р	
	Response	Р	Р	Р	Р	Р	Р	Р	Р	
Overall		Р	Р	Р	Р	Р	P(80%)	Р	Р	

P-PassedF-Failed

5. Summary

- Developed interoperability test specification draft of PMUs including:
 - PMU interoperability test methods
 - PMU interoperability test suite and six test cases
 - PMU Interoperability test report template
- Conducted interoperability tests of 8 commercial PMUs
- Provided preliminary results of interoperability test of 8 commercial PMUs
- Future plans:
 - conduct tests of additional commercial PMUs,
 - verify the interoperability test methods, and
 - standardize the interoperability test specification for PMUs to support interoperability certification of PMUs.

Interoperability Test Specification Draft of IEEE C37.118.2 Standard-based PMUs

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	B.2.2 Header Frames	1
	B.2.3 Configuration Frame 1 (CFG-1)	1
	B.2.4 Configuration Frame 2 (CFG-2)	
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Questions???

